Trip

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Laurence B. Boucher, et al.

Ser. No:

10/678,336

Filing Date:

October 3, 2003

Examiner:

Unknown

Atty. Docket No:

ALA-008H

GAU:

Unknown

For:

METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING

WITH A HIGH PERFORMANCE NETWORK INTERFACE

March 3, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

5th Supplemental Information Disclosure Statement

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring sixteen documents listed on an enclosed one-page form PTO-1449 to the attention of the Examiner in the above-identified application.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Copies of the documents listed on the enclosed one-page form PTO-1449 are not submitted because they were submitted in an earlier application (10/005,536) which is relied upon for an earlier filing date under 35 U.S.C. §120.

Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on March 3, 2005.

Date: 3-3-05

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(TPE)							Sheet 1 of
U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/678,336		
MAR 0 7 2005 E					Filing date: October 3, 2003		
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE					Inventors: Laurence B. Boucher, et al.		
					Group Art I	Unit: Unknown	
					Examiner n	ame: Unknown	
					Attorney De	ocket No. ALA-	-008H
			U.S.	. Patent Documents			
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	Α	5,524,250	6/4/96	Chesson et al.	395	775	
	В	5,619,650	4/8/97	Bach et al.	395	200.01	
	С	5,727,142	3/10/98	Chen	395	181	
	D	5,802,258	9/1/98	Chen	395	182.08	
	Е	5,898,713	4/27/99	Melzer et al.	371	53	
	F	6,021,507	2/1/00	Chen	714	2	
	G	6,047,323	4/4/00	Krause	709	227	
	Н				T		
		OTHER	ART—NON P/	ATENT LITERATURE (DOCUMENT	เร	
Examiner Initial	Cite No.		(Includ	ding Author, Title, Date, I	Pertinent Pa	ges, Etc.)	
	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.					
	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.					
	3	Chesson et al., IEEE Syposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.					
	4	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.					
	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.					
	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.					
	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.					
	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.					
	9	Lilienkamp et al	I., Publication e	entitled "Proposed Host-	-Front End P	rotocol", 56 p	ages, Dec. 1984.
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in conformance and not considered. Include copy of this form with your communication to applicant.